



cket Number (Optional)  
81784.0233

**Application Number**  
09/823,328

# INFORMATION DISCLOSURE CITATION IN AN APPLICATION

(Use several sheets if necessary)

**Applicant**

TSUTSUI, et al.

**Filing Date**

**March 29, 2001**

## Group Art Unit

2816

## U.S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
<i>Dr</i>	10-304653	11/13/1998	JAPAN			Abstract	
<i>Dr</i>	002199006	11/13/1998	JAPAN			Abstract	

**OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Pages, Etc.)

	Hirata, et al. "New Plus-and Minus-Voltage Generators for TFT-LCD Panels" Proceedings of Second IEEE Asia Pacific Conference on ASICS, 28-30 August 2000, pgs. 17-20

**EXAMINER**

DATE CONSIDERED

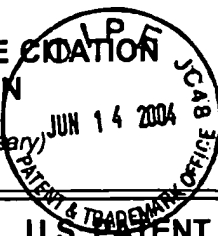
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Applicant

Yusuke TSUTSUI et al.

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## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
<i>R</i>	*5,986,649	11/16/1999	Suguru Yamazaki			

## FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
<i>R</i>	W096/21880 (*corresponds to U.S. Patent 5,986,649)	07/18/1996	PCT				
<i>R</i>	W099/42894 (*corresponds to EP 0 990 940 A1)	08/26/1999	Europe				

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)


EXAMINER

*Thalbal*

DATE CONSIDERED

*1/06/05*

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